

Electronic Acknowledgement Receipt

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International Application Number:	
Confirmation Number:	9388
Title of Invention:	Logical operation circuit and logical operation method
First Named Inventor/Applicant Name:	Michitaka Kameyama
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Petition for review by the Office of Petitions.	819190020-PET-FOR-REVI VAL.PDF	14756 7afced76b8321308104f1139b19f62a73 a0c5ba0	no	2
Warnings:					
Information:					
2	Supplemental Response or Supplemental Amendment	819190020-RESP-NON-CO MPL-AMDM.PDF	15934 45cde0f189b4fa67586f706493576e61 31d4cec	no	2
Warnings:					
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3	Oath or Declaration filed	819190020DECLARATION. PDF	28937 51bc2618c2137f6f47488ad331be07cf4 230061b	no	2
Warnings:					
Information:					
4	Miscellaneous Incoming Letter	819190020CopyofTermDiscl aimer.PDF	25502 15b9426700fd7e2114f3db8a84e4da4b3 c9d9535	no	3
Warnings:					
Information:					
5	Fee Worksheet (PTO-06)	fee-info.pdf	8177 af9a7222a0b8d8e203449976b0f8648c 78e109d5	no	2
Warnings:					
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Total Files Size (in bytes):			93306		
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